

Sheet 1/ 24

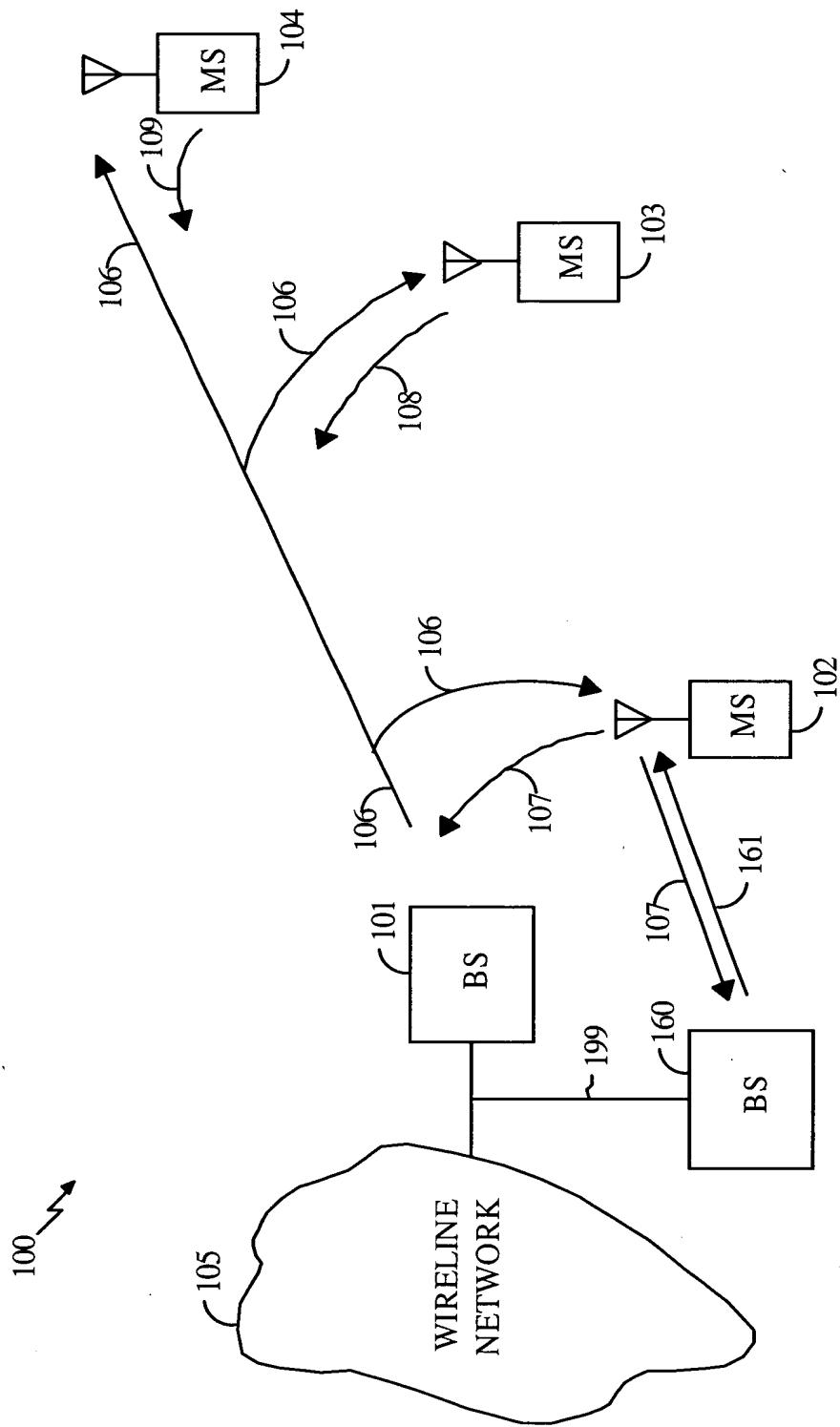


FIG. 1

Sheet 2/24

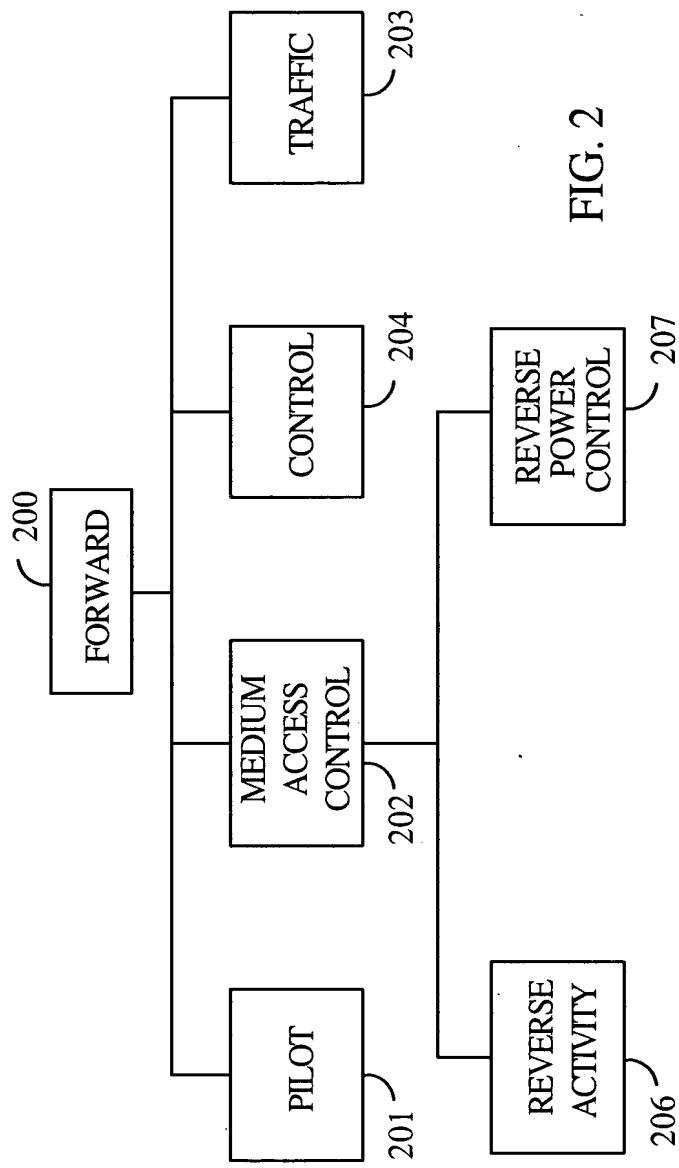


FIG. 2

Sh et 3/ 24

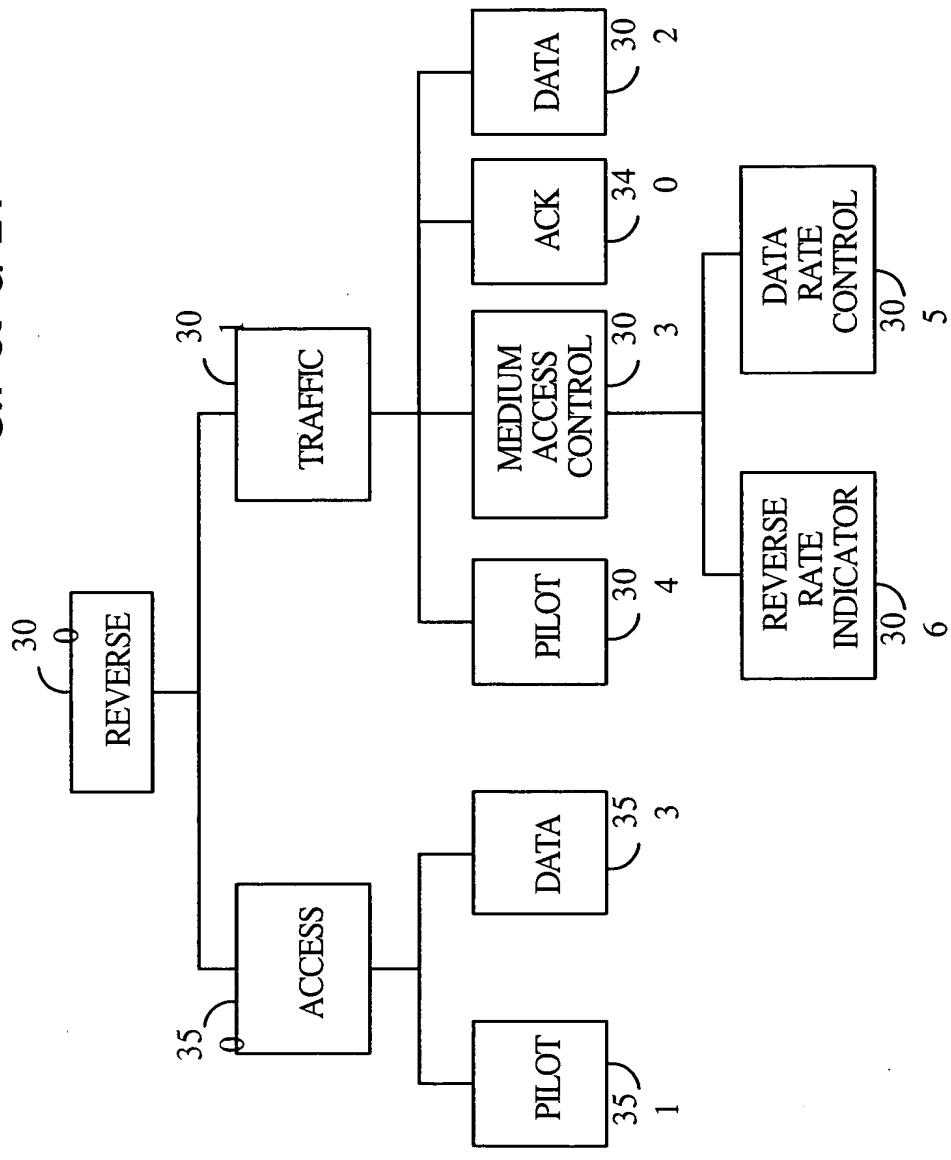


FIG. 3

Sheet 4/ 24

SIGNAL AMPLITUDE

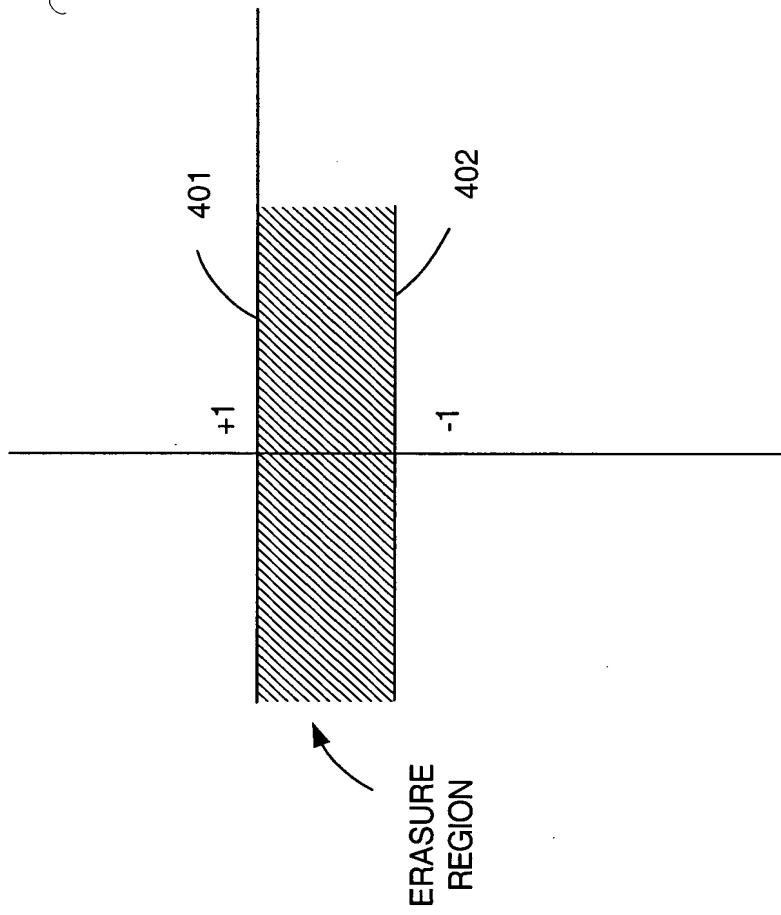


FIG. 4

Sheet 5/ 24

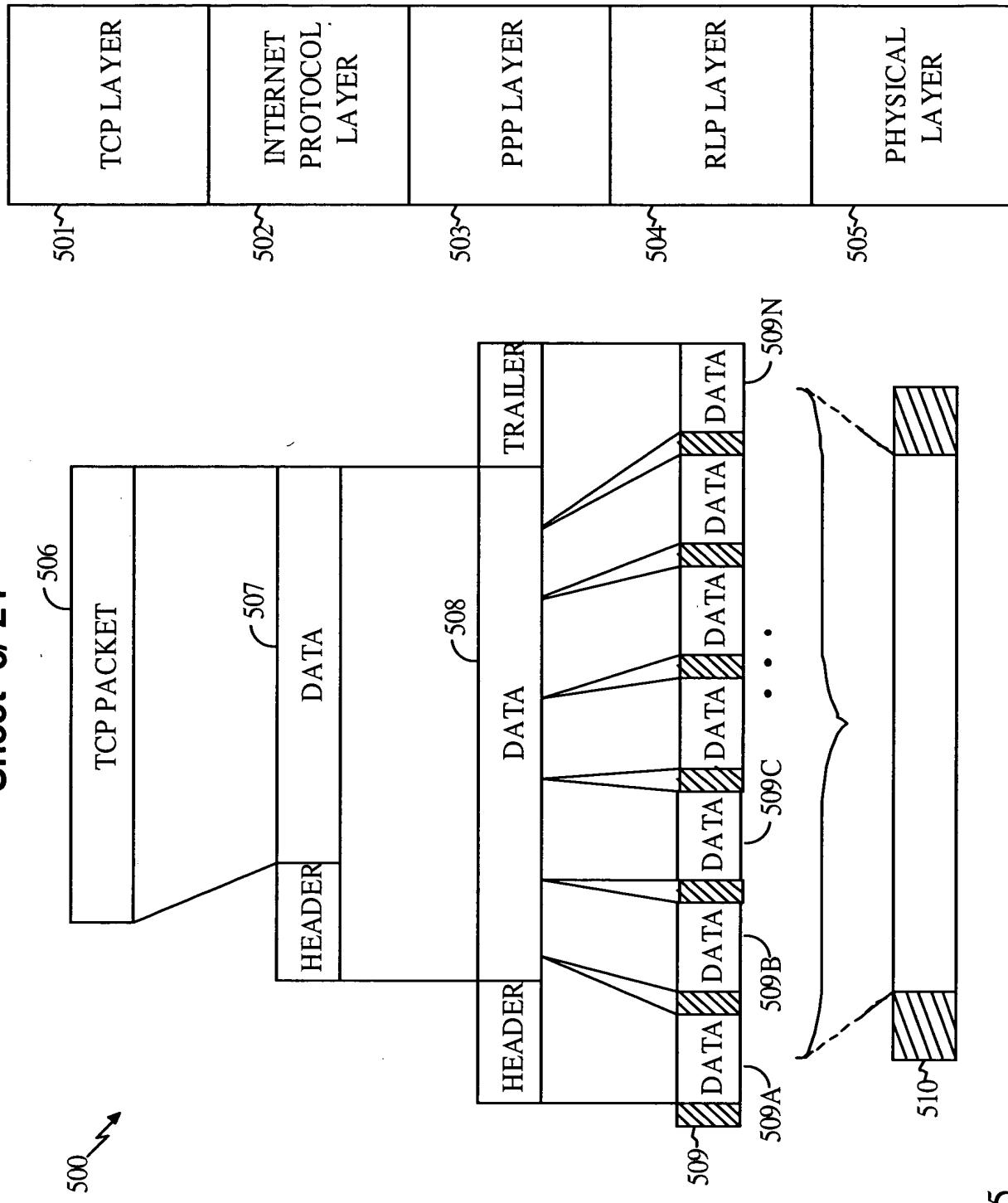


FIG. 5

Sheet 6/ 24



DRC VALUE	RATE (kbps)	PACKET LENGTH (SLOTS)
0x0	NULL RATE	N/A
0x1	38.4	16
0x2	76.8	8
0x3	153.6	4
0x4	307.2	2
0x5	307.2	4
0x6	614.4	1
0x7	614.4	2
0x8	921.6	2
0x9	1228.8	1
0xa	1228.8	2
0xb	1843.2	1
0xc	2457.6	1
0xd	INVALID	N/A
0xe	INVALID	N/A
0xf	INVALID	N/A

FIG. 6

Sheet 7/24

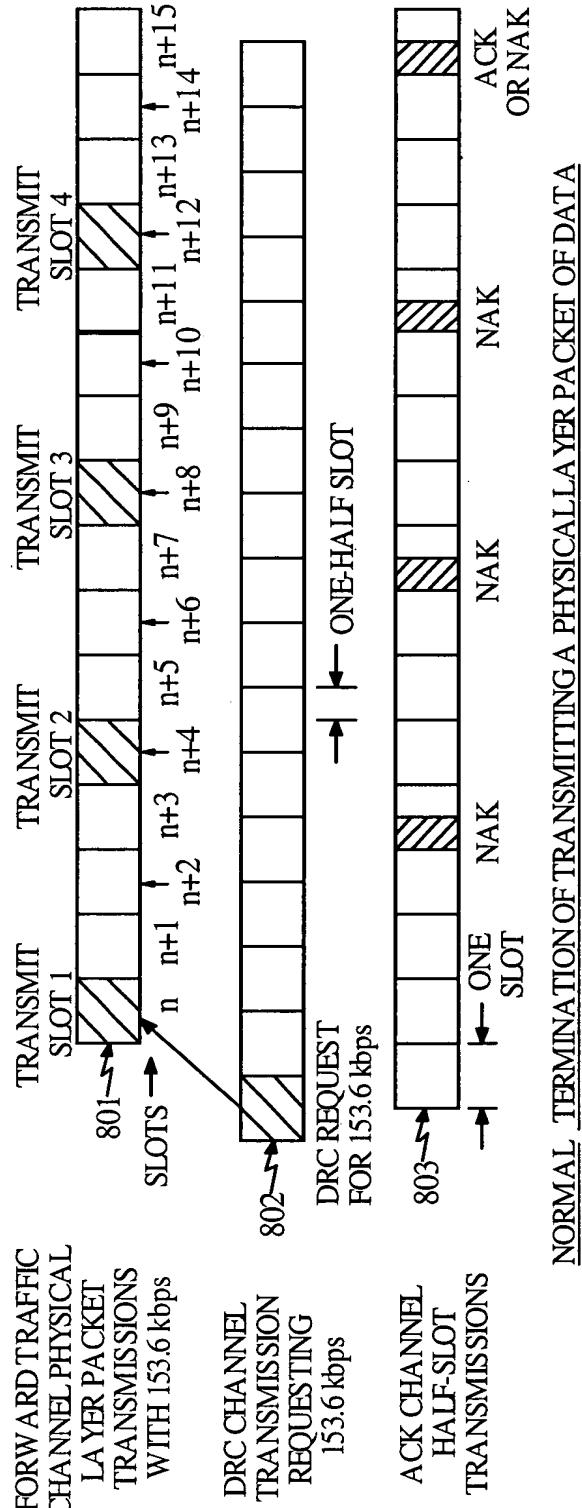
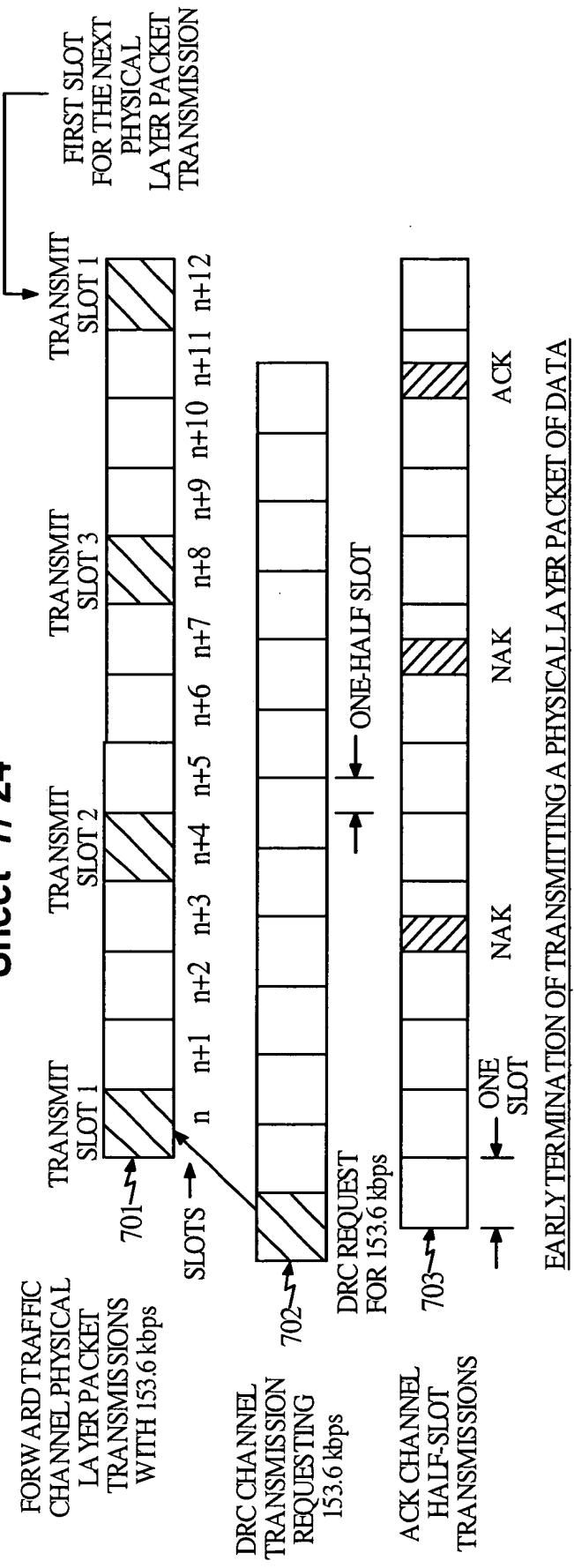
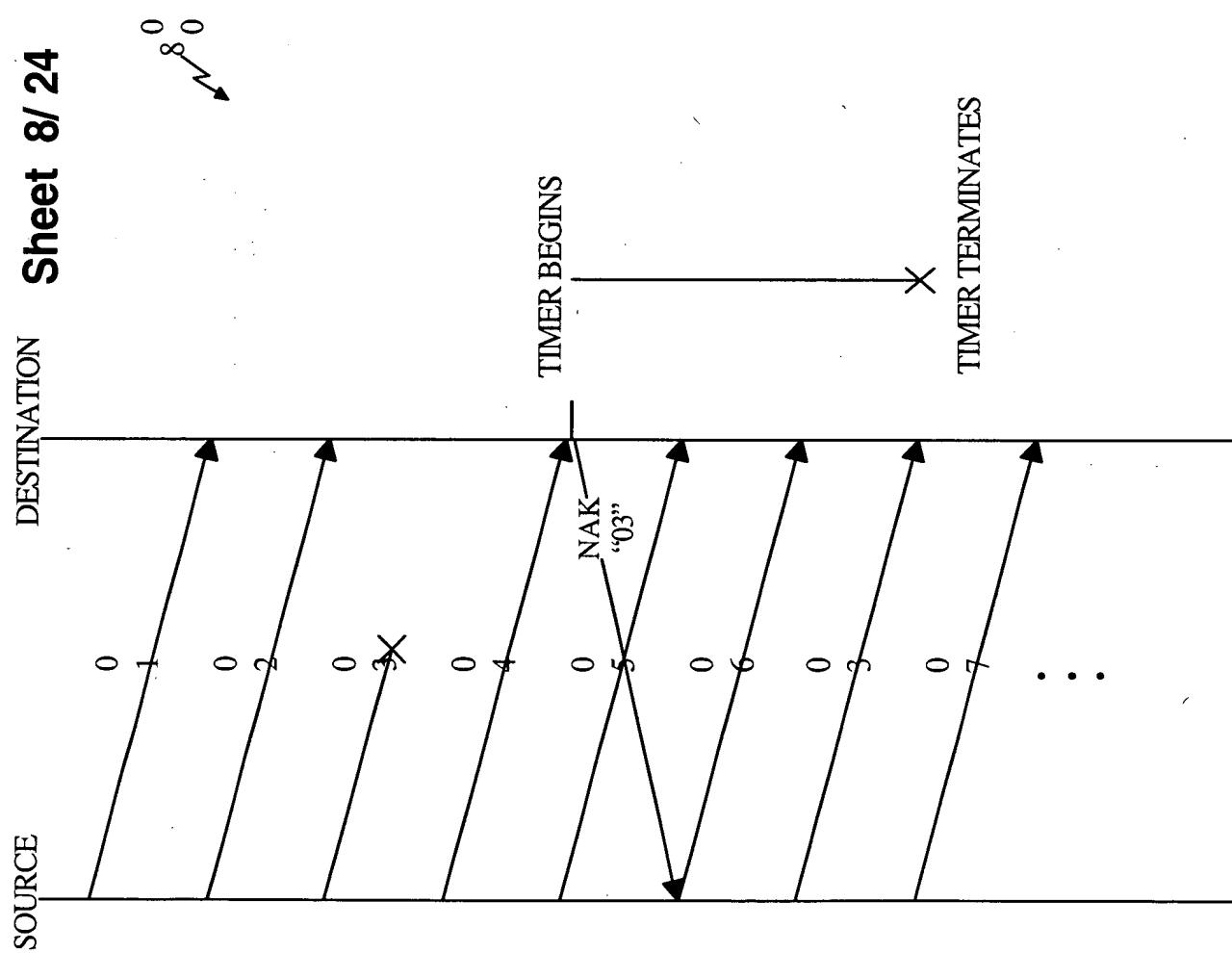


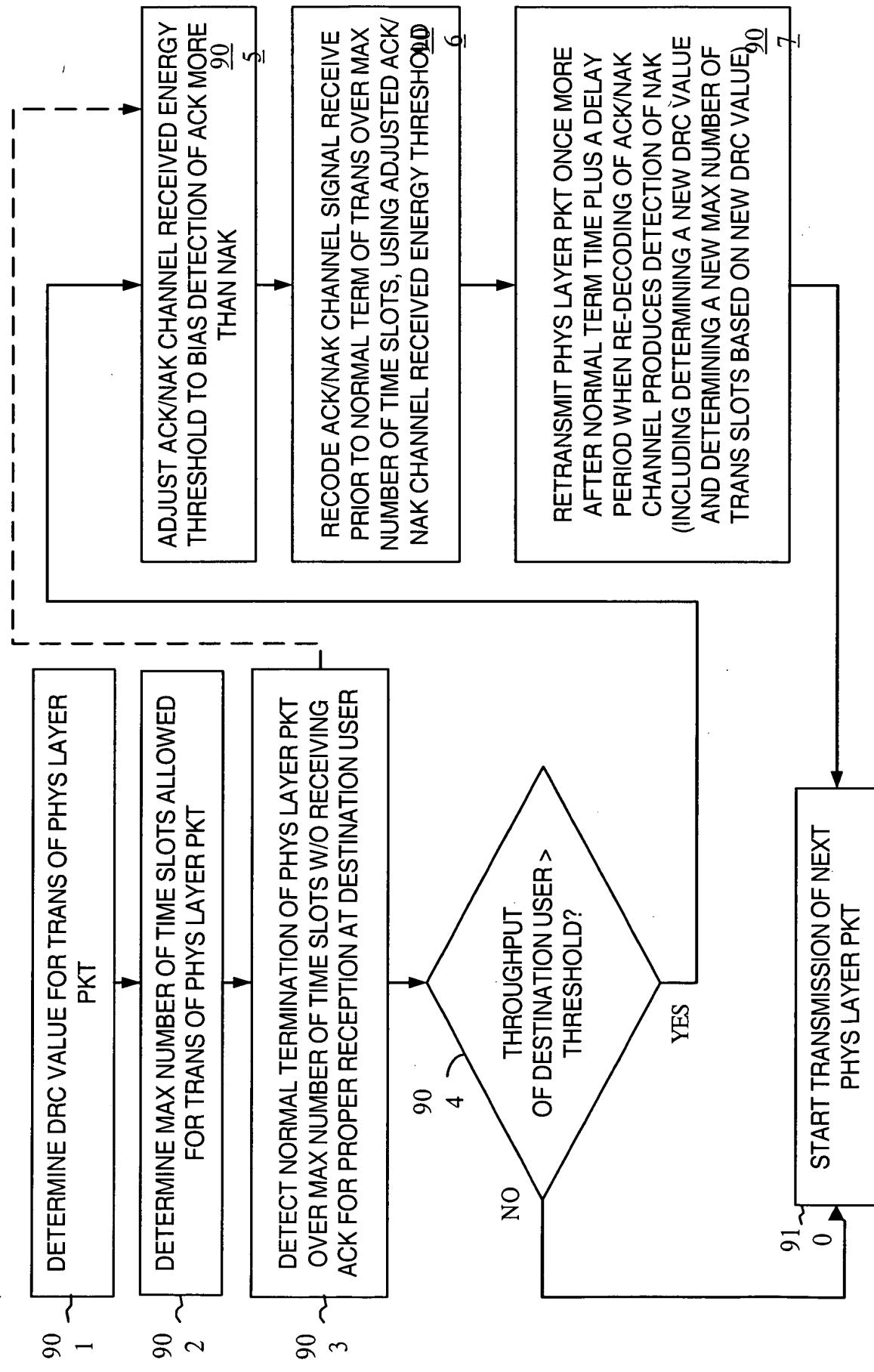
FIG. 7



8
FIG.

Sheet 9/ 24

FIG. 9



Sheet 10/ 24

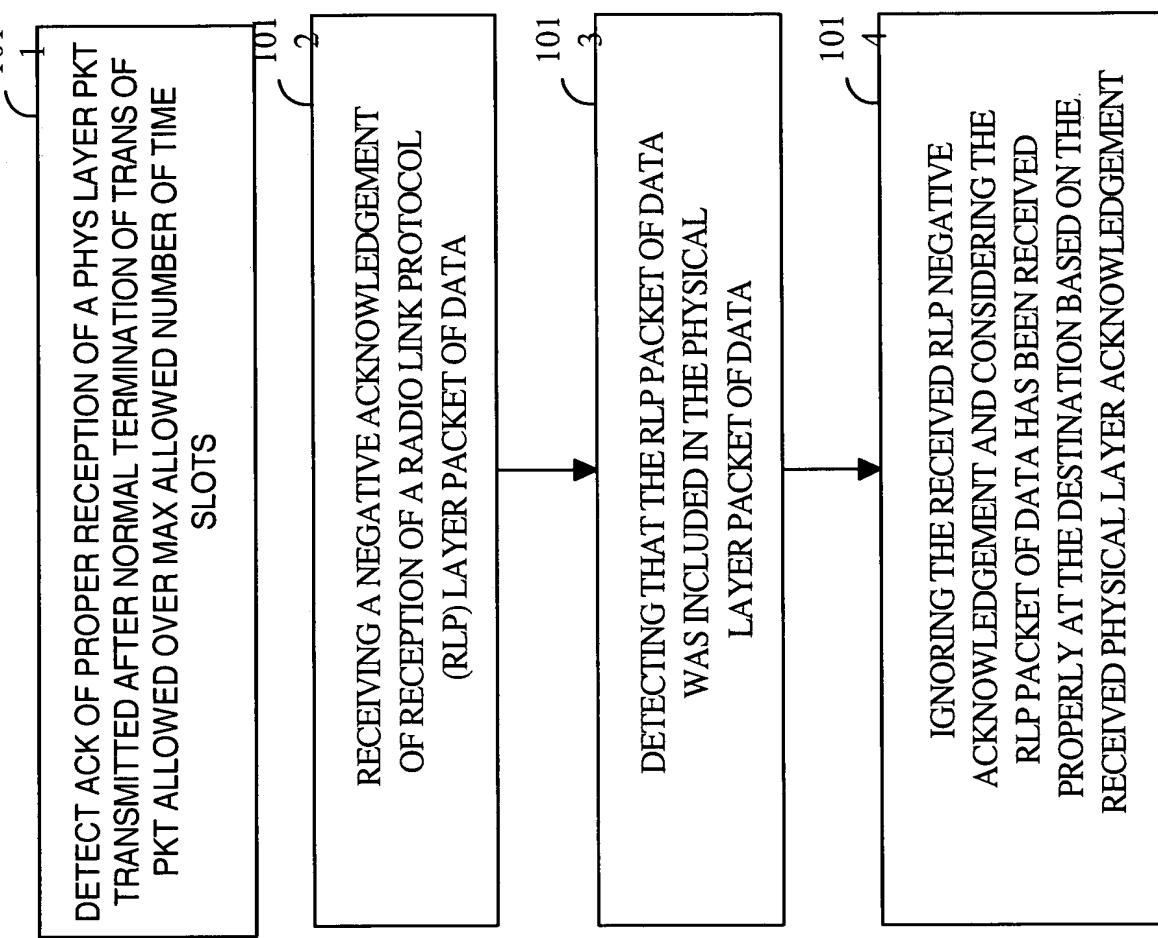


FIG. 10

Sheet 11/ 24

1200

1210

CONTROL
SYSTEM

1206

SEARCHER

1208

FINGER ELEMENT

1208

FINGER ELEMENT

1212

DECODER

1214

FINGER ELEMENT

+

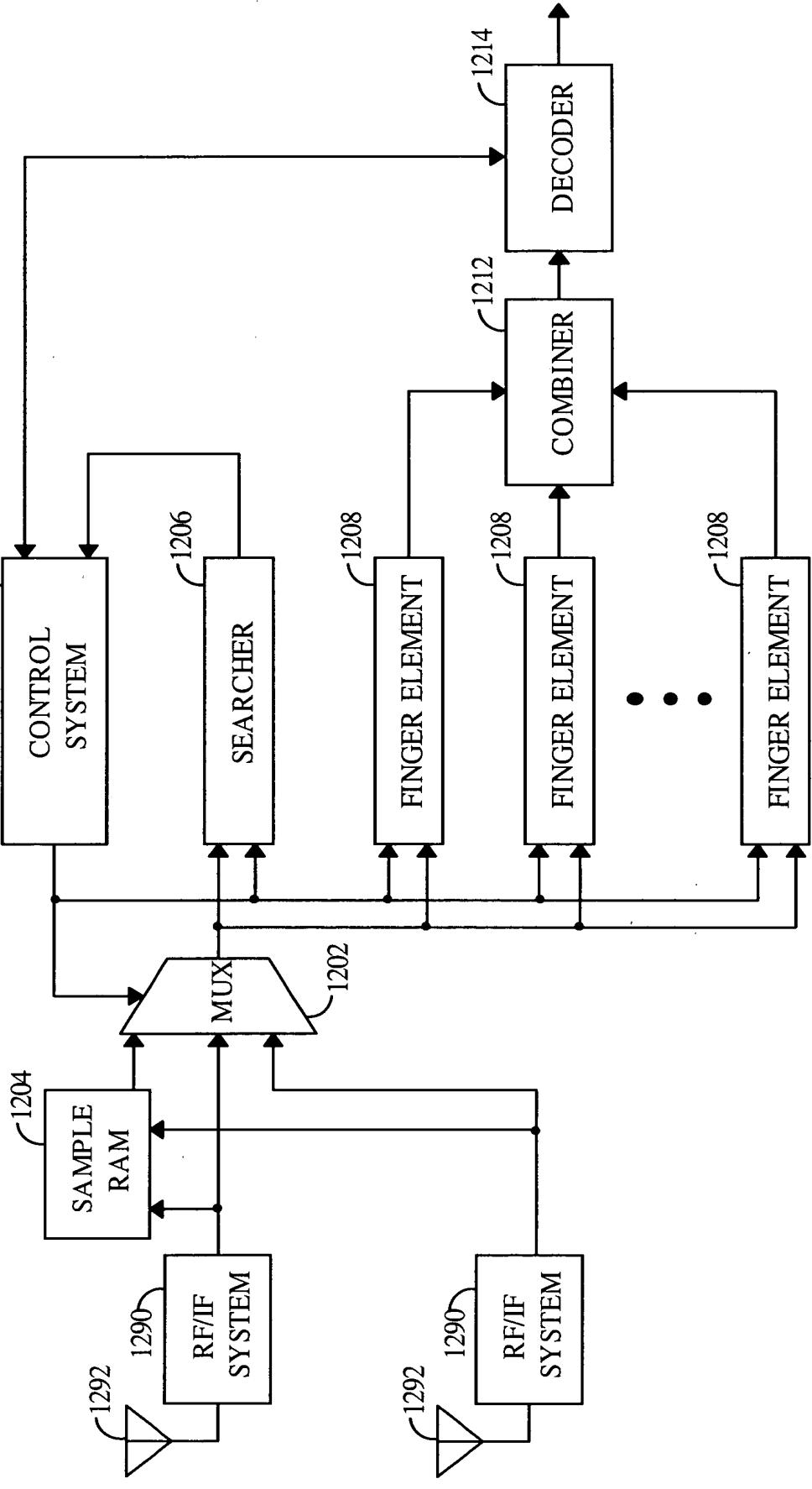


FIG. 11

+

Sheet 12/ 24

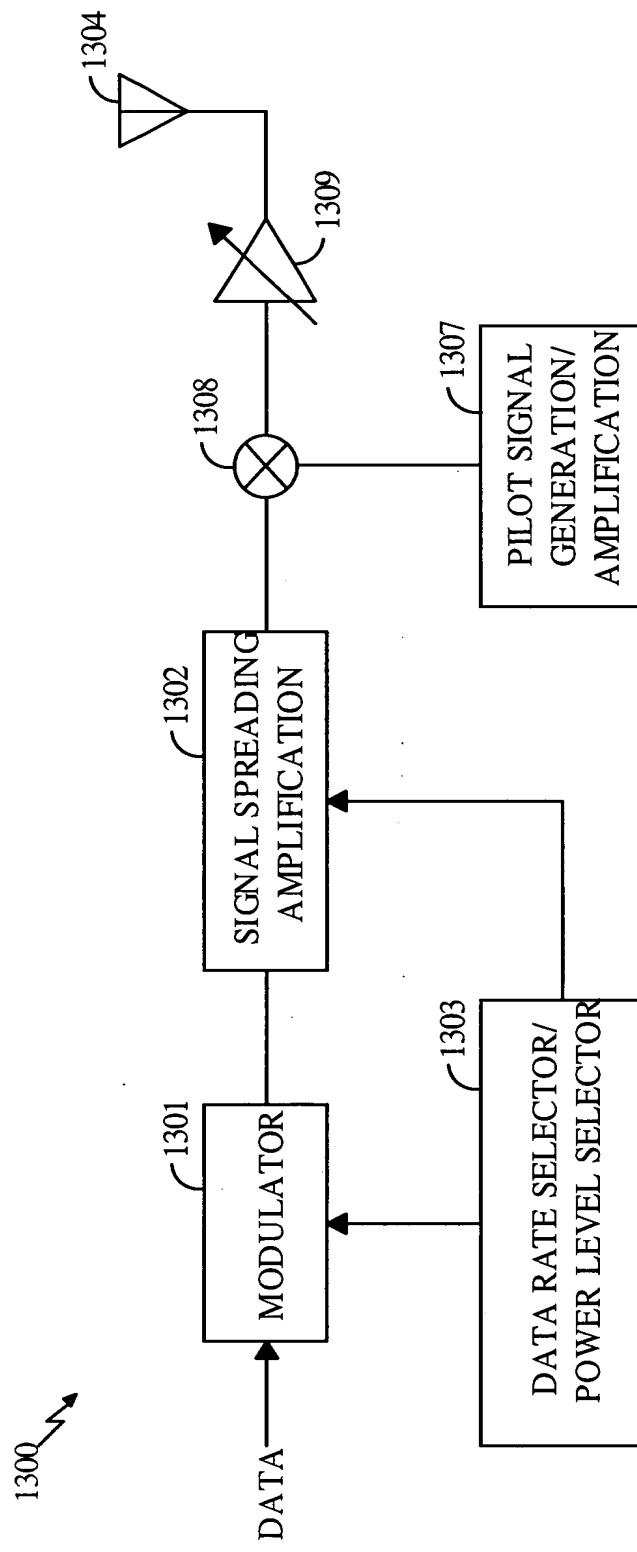


FIG. 12

Sh et 13/ 24

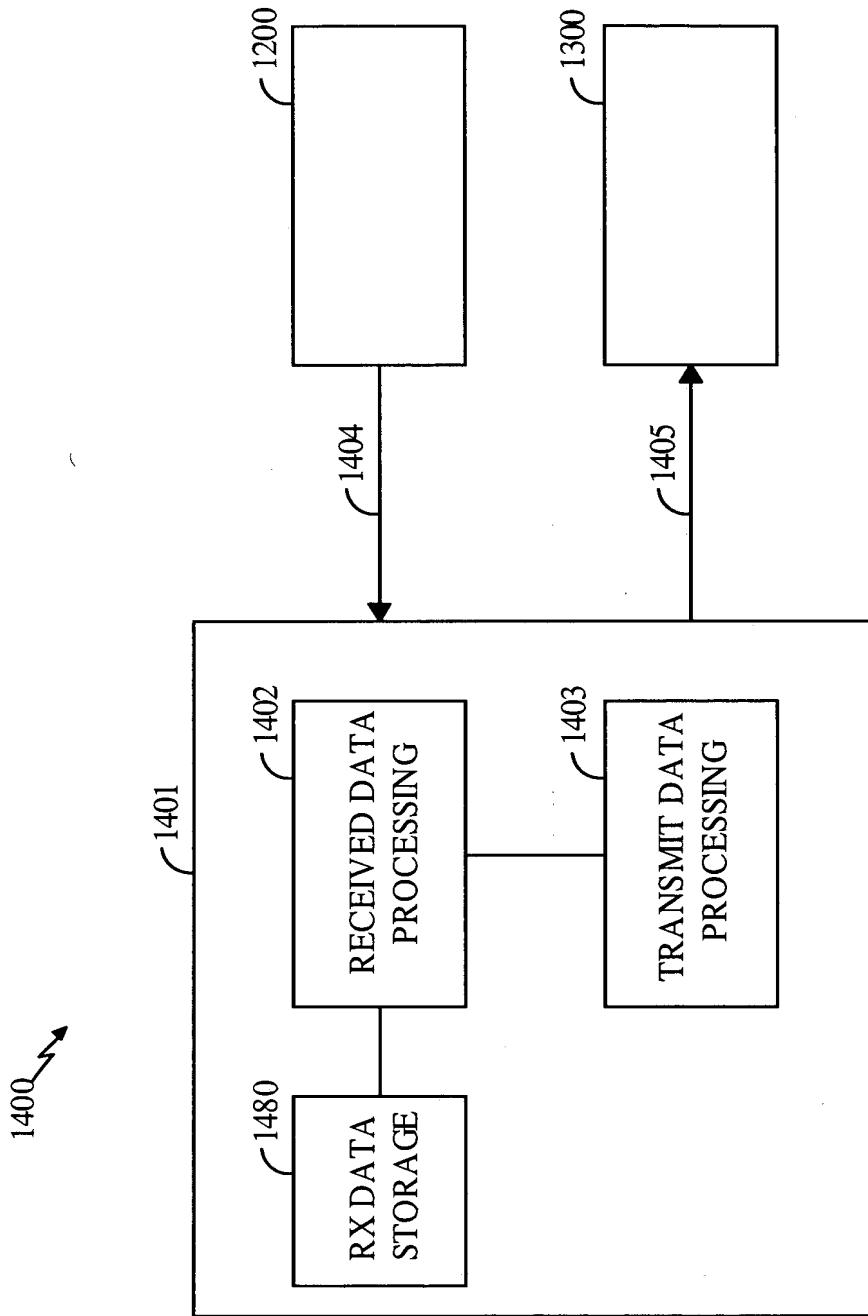


FIG. 13

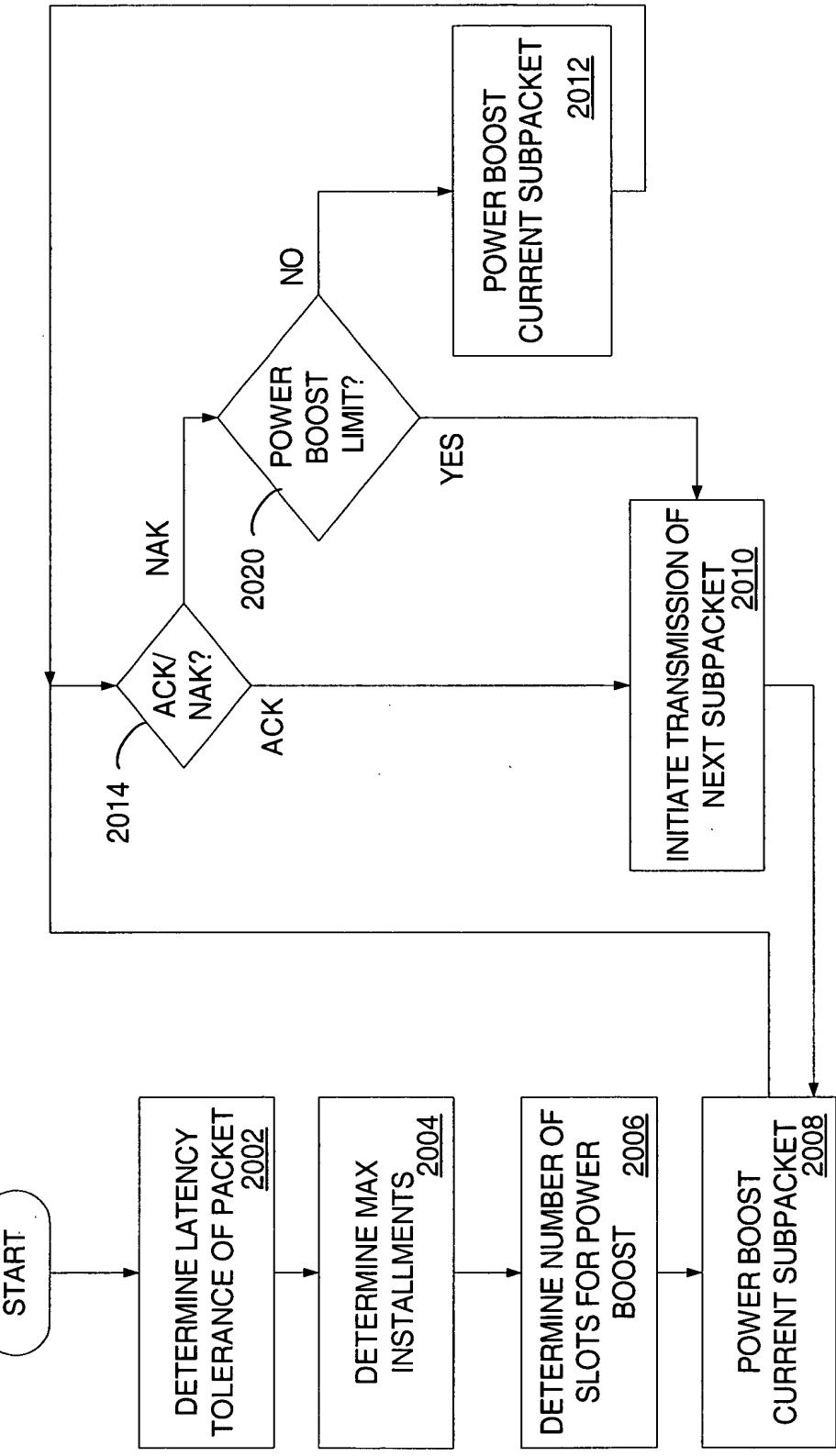


FIG. 14

+

Sheet 15/ 24

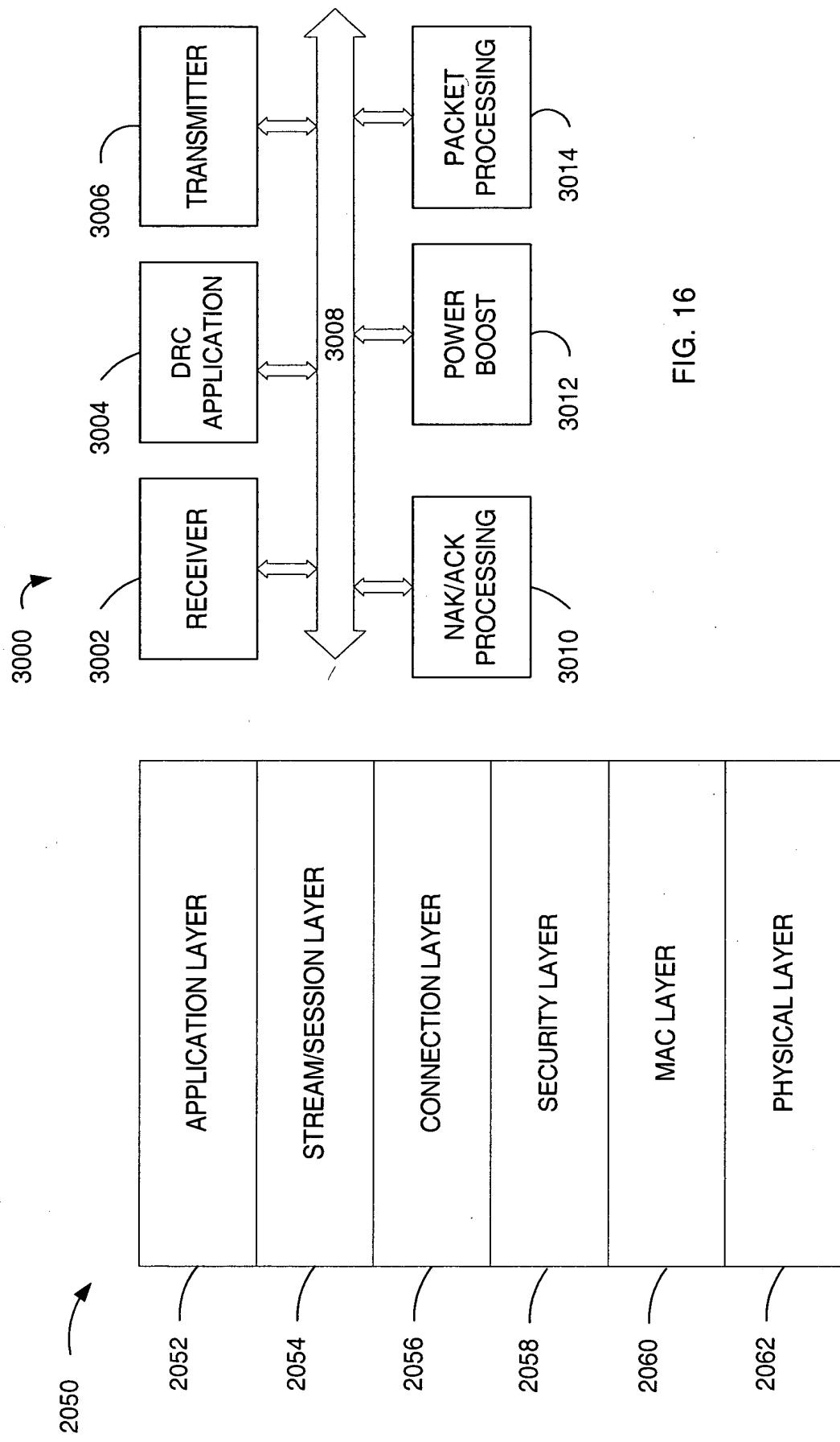


FIG. 15

FIG. 16

+

Sheet 16/ 24

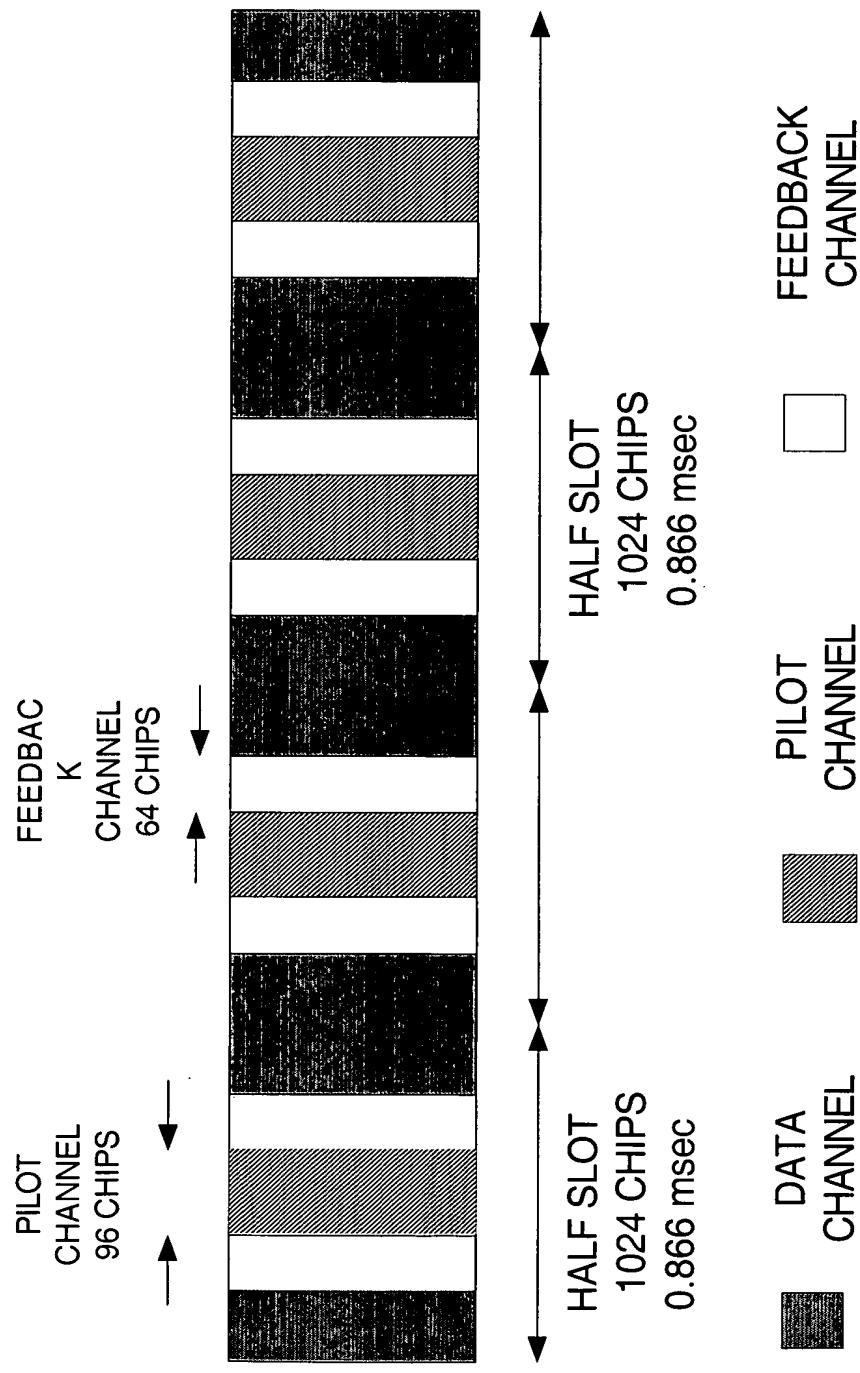


FIG. 17

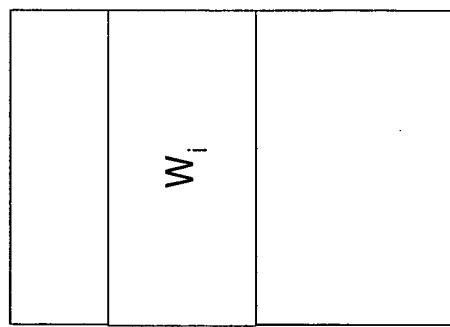


FIG. 18

Sheet 17/ 24

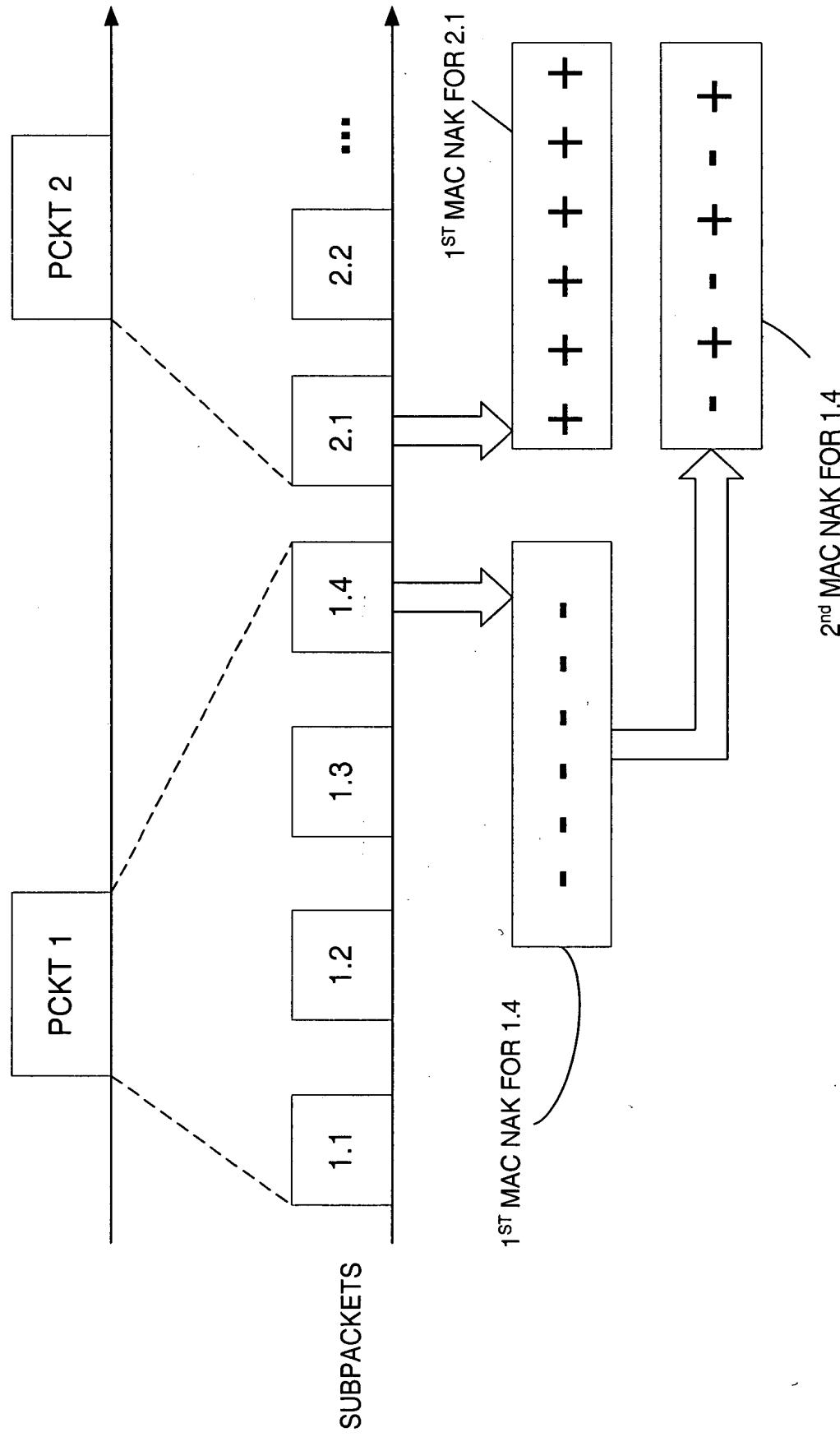
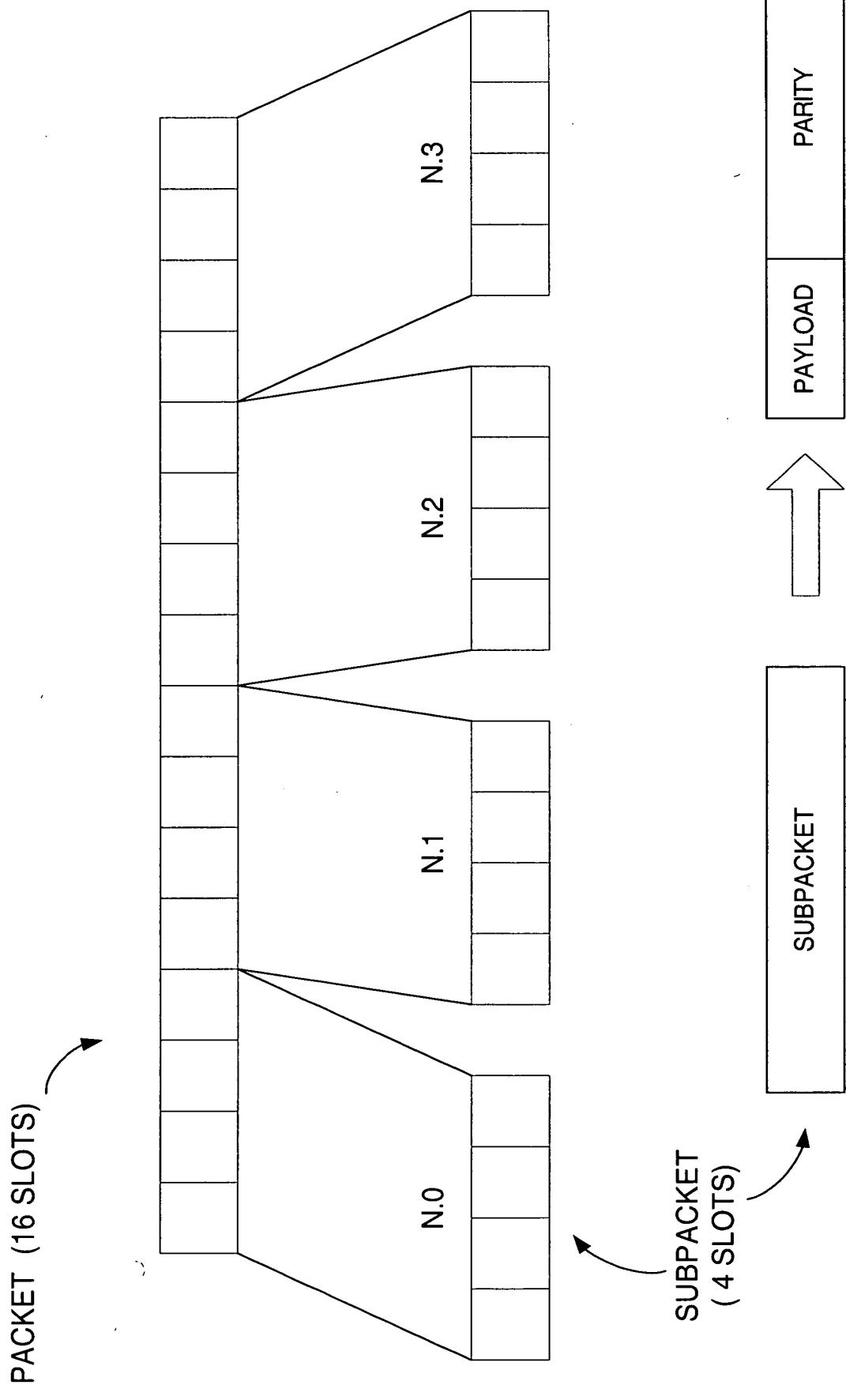


FIG. 19

Sheet 18/ 24



Sheet 19/ 24

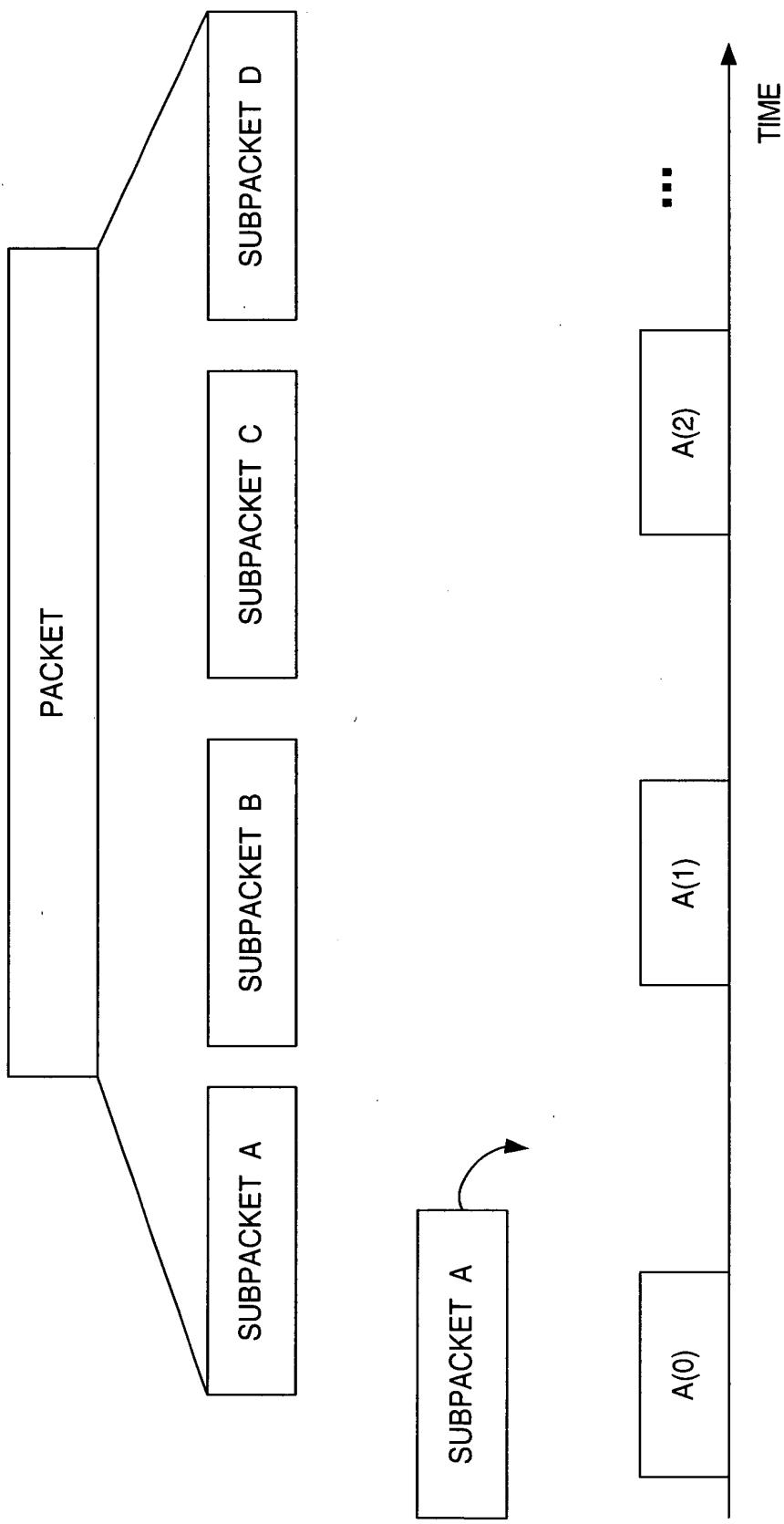


FIG. 21

Sheet 20/ 24

PAYLOAD SIZE (bits)	EFFECTIVE DATA RATE (kbps)			
	AFTER 1 SUBPACKET	AFTER 2 SUBPACKETS	AFTER 3 SUBPACKETS	AFTER 4 SUBPACKETS
[1] 128	19.2	9.6	6.4	4.8
[2] 256	38.4	19.2	12.8	9.6
[3] 512	76.8	38.4	25.6	19.2
[4] 768	115.2	57.6	38.4	28.2
[5] 1024	153.6	76.8	51.2	38.4
[6] 1536	230.4	115.2	76.8	57.6
[7] 2048	307.2	153.6	102.4	76.8
[8] 3072	460.8	230.4	153.6	115.2
[9] 4096	614.4	307.2	204.8	153.6
[10] 6144	921.6	460.4	307.2	230.4
[11] 8192	1228.8	614.4	409.6	307.2

FIG. 22

Sheet 21/ 24

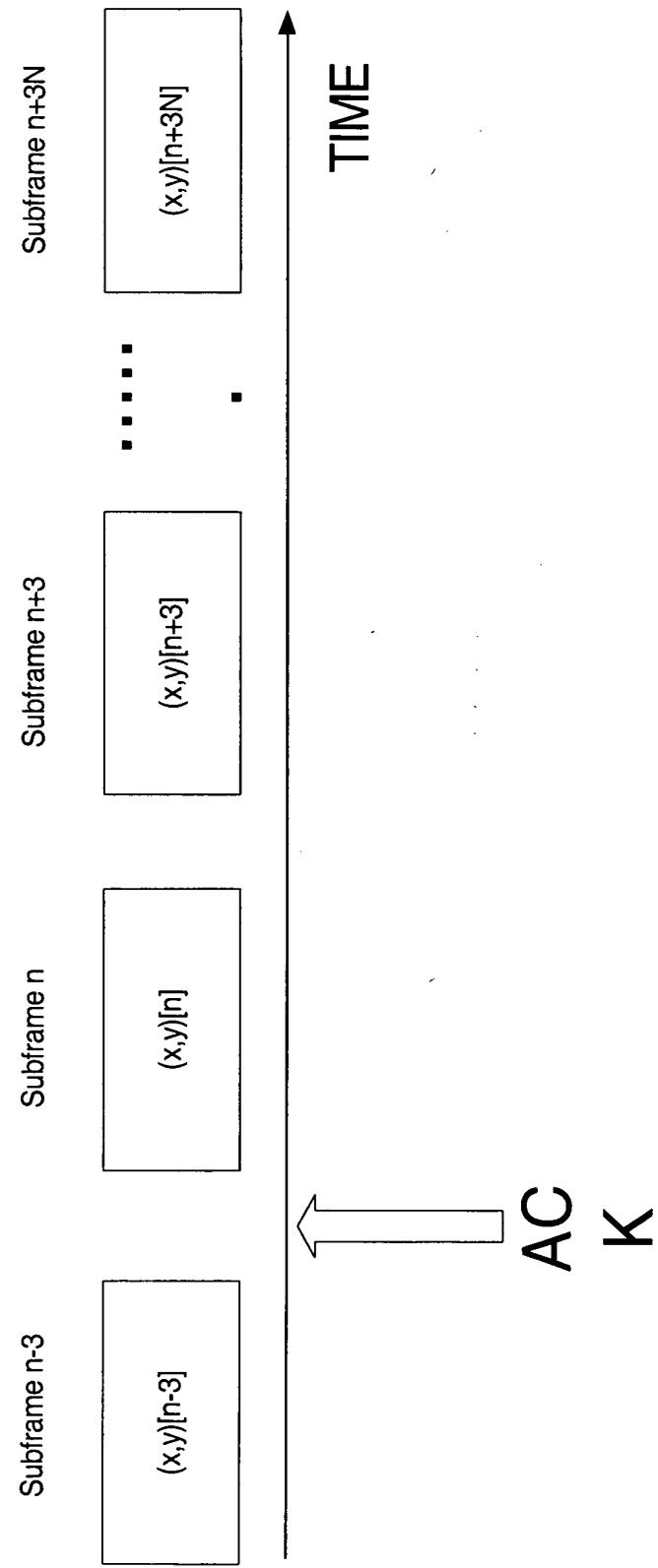


FIG. 23

Sheet 22/ 24

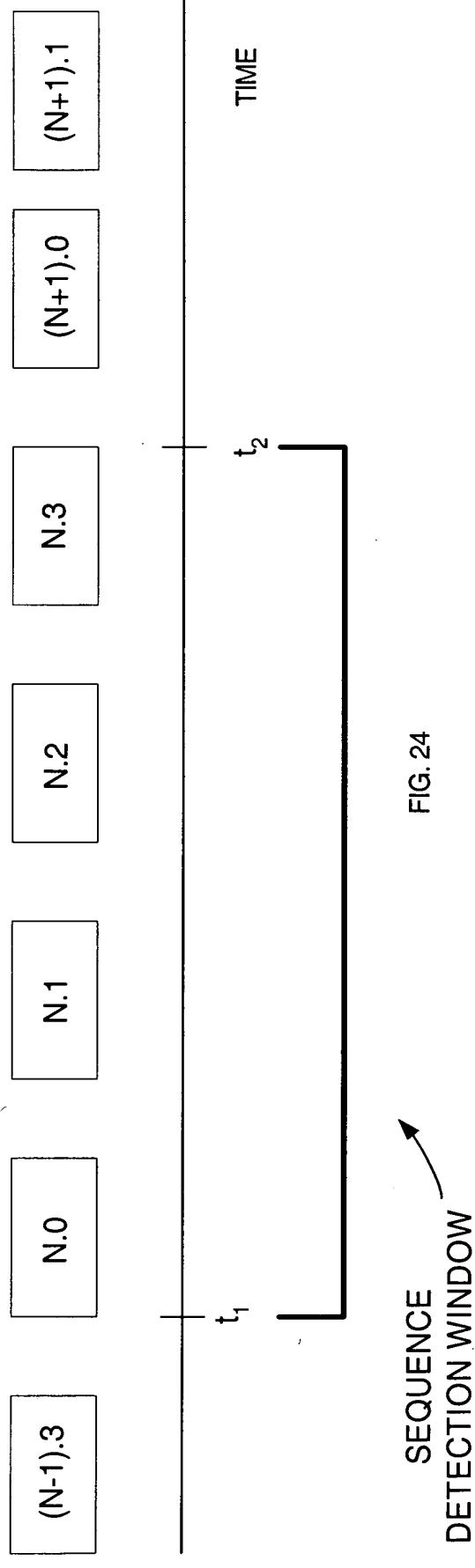


FIG. 24

SEQUENCE
DETECTION
WINDOW

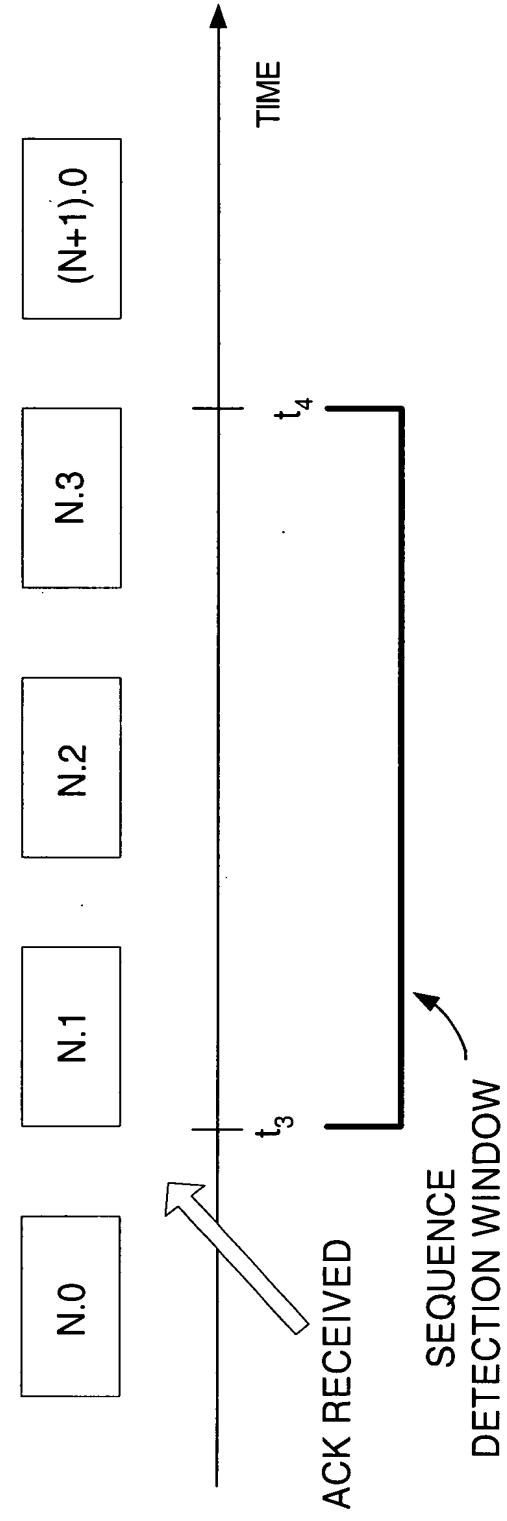


FIG. 25

Sheet 23/ 24

3000

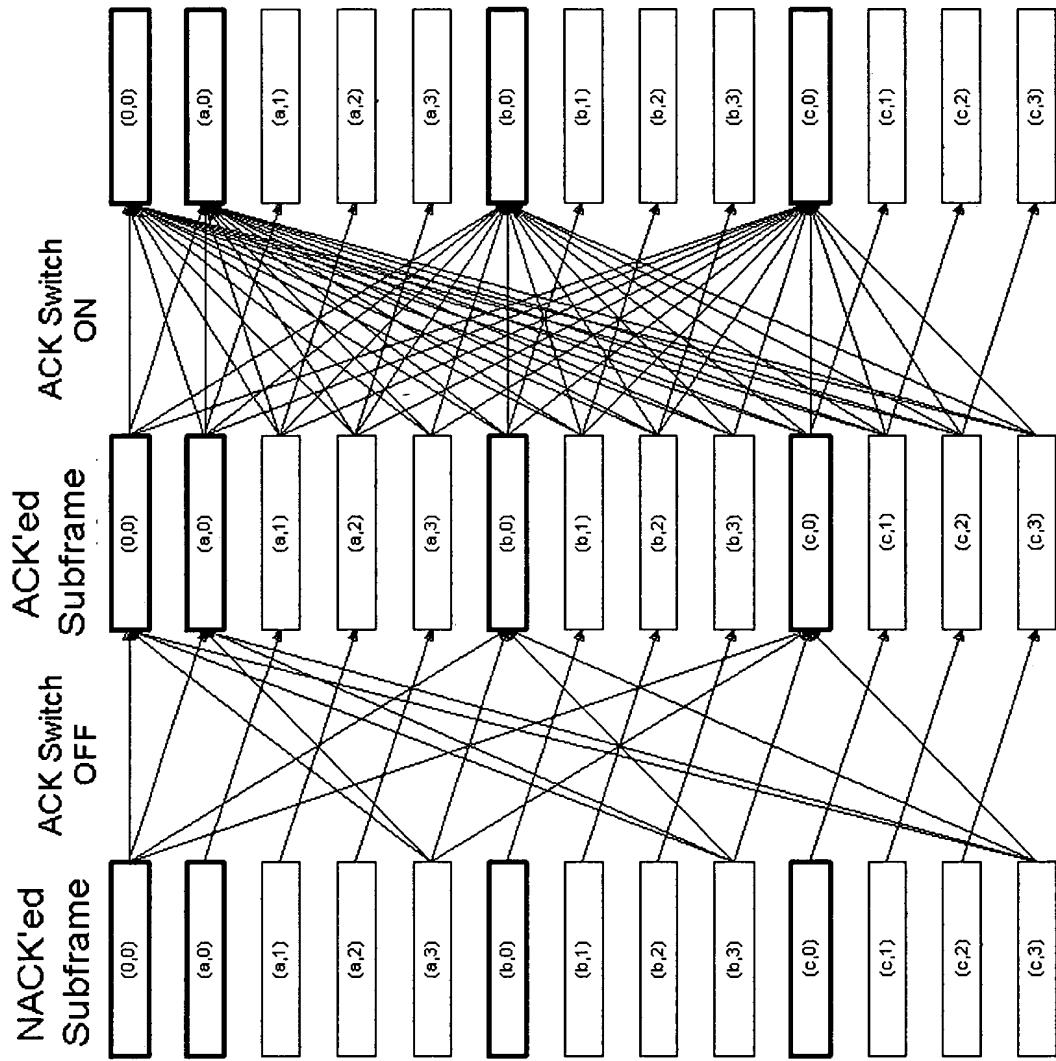


FIG. 26

Sheet 24/ 24

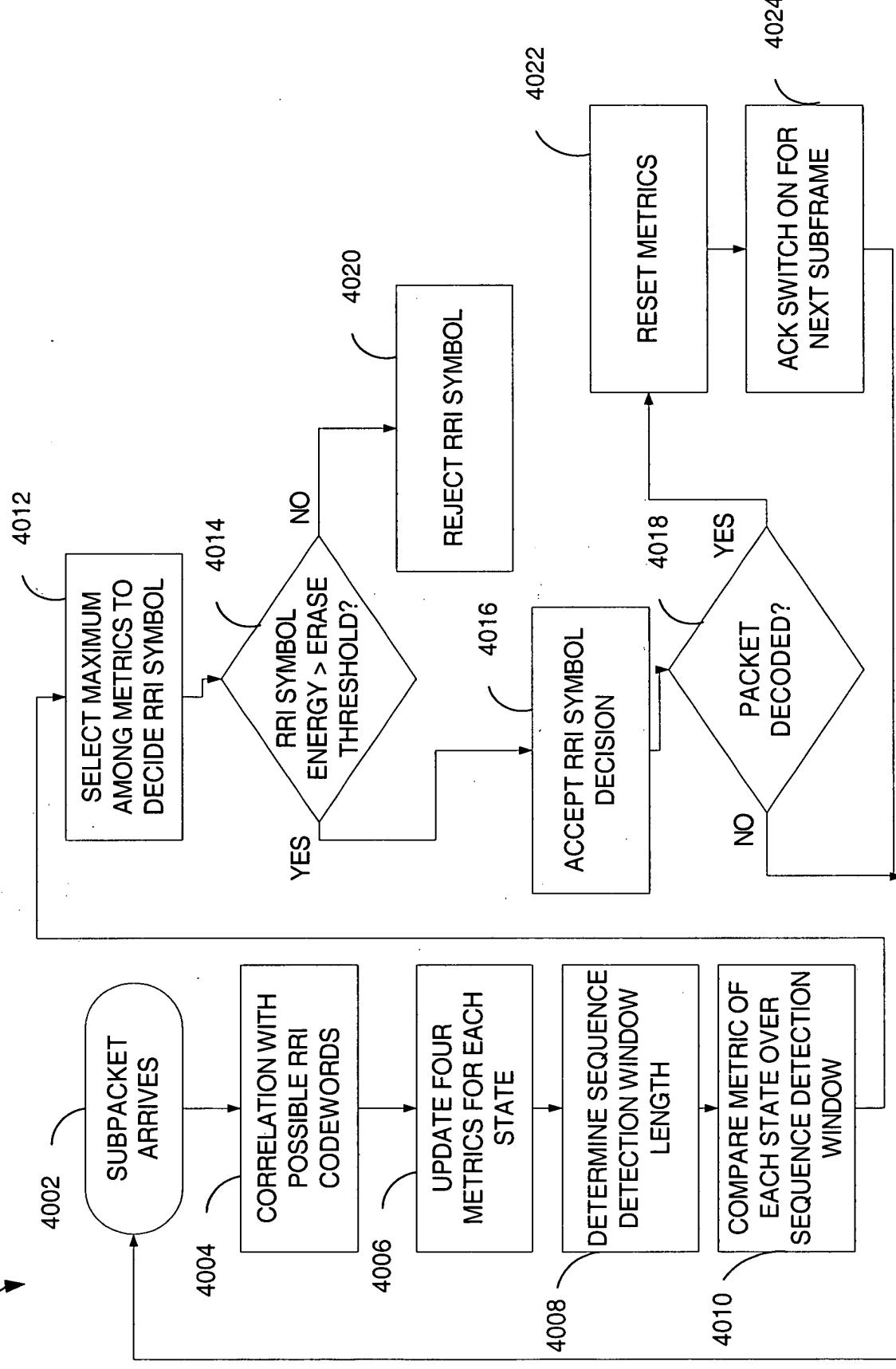


FIG. 27